



(TRANSLATION)

Reference No. TD99P04697

Dispatch No. 230533
Dispatch Date: June 28, 2005

Notification of Reason(s) for Refusal

Patent Application No.: Pat. Appln. 2003-037474
Drafting Date: June 20, 2005
Examiner of JPO: Hiroyuki Hatanaka 9180 5W00
Representative of Applicant for Patent: Katsumi Hoshimiya
Applied Provision: Patent Law Section 29(2)

This application should be refused for the reason mentioned below. If the applicant has any argument against the reason, such argument should be submitted within 60 days from the date on which this notification was dispatched.

Reason

The invention(s) in the claim(s) listed below of the subject application should not be granted a patent under the provision of the Patent Law Section 29 (2) since it could have easily been made, prior to the filing of the patent application, by a person with ordinary skill in the art to which the invention pertains, on the basis of inventions which were described in the publications listed below or made available to the public through electric telecommunication lines in Japan or elsewhere prior to the filing of the patent application.

Note (See the list of cited documents.)

Claims: 1, 5-8, 12-15, 19-23

Cited documents: 1, 2

Remarks:

When integrating an acoustic wave element on a substrate, whether to mount the element via a mounting board on a multilayer substrate or to mount the element directly on the multilayer substrate is a matter of design which can be appropriately determined by a person skilled in the art as necessary.

Furthermore, as disclosed in the cited reference 2 (see paragraph [0082]), it is well known to configure a front end module to be operable in four or more

frequency bands. Therefore, it would have been easy for a person skilled in the art to apply the well-known technique and the narrow-band code division multiple access system used in the standards of IS-95 and cdma-one to the cited reference 1 to provide a second duplexer.

Claims: 2-4, 9-11, 16-18

Cited documents: 1-6

Remarks:

As disclosed in the cited references 3-6, it is a widely used technique in the art to integrate a filter for allowing either transmission signals or reception signals to pass, a power amplifier, an antenna, etc. on a multilayer substrate.

For the claim(s) other than the claims specified in this notification of reasons for refusal, no reason(s) for refusal has been found so far. If any reason(s) for refusal is found later, it will be notified.

List of cited documents

1. Pamphlet of PCT Publication No. WO 02/001741
2. JP Pat. Application Publication No. 2002-33676
3. JP Pat. Application Publication No. 2001-127652
4. Pamphlet of PCT Publication No. WO 00/52841
5. JP Pat. Application Publication No. Hei 10-303640
6. Pamphlet of PCT Publication No. WO 02/003499